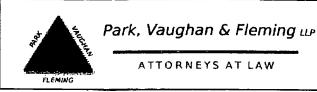
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FROM: Panos Arvanitis	DATE: 10/20/2008 6:23 PM
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TOTAL NO. OF PAGES INCLUDING COVER:	
RE: Interview Agenda for App. 10/667, 115	

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NOTES/COMMENTS:

Ameliantian Nisashaa

Application Number: 10/667,115

Confirmation Number: 8531

Applicant

Filed

26/2008 17:38

: Russell N. Mirov : 19 September 2003

T.C./A.U.

: 2841

Examiner

: Dinh, Tuan T.

Docket Number

: SUN03-0112

Customer No.

: 57,960

Interview Agenda

Via Fax (571) 273-1929

INTERVIEW AGENDA

Dear Examiner Dinh:

In light of scheduling an interview at your earliest convenience, please find the interview agenda below. Applicant wishes to discuss the following points:

1. Key area of substrate

- Includes identification mechanism which uniquely identifies the key area as being originally attached to the circuit board.
- May include an electronic identification chip which contains an identification code that uniquely identifies the key as being originally attached to the circuit board.

2. Signal trace

- Routed from the circuit board through the key area and back to the circuit board.
 - Conducts a signal required for a normal operation of the circuit board.
 - Permanently severed when the key is removed from the circuit board.

3. Removal features

- At least one of slits, slots, gaps, bores, or weakened or thinned parts.

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4. Other features

- Electronic ID chip is readable only when the key is detached from the circuit board (in some embodiments).
 - JC on circuit board detects absence of the key (in some embodiments).
 - IC may disable some operations of the circuit board.

Respectfully submitted,

Anthony P. Jones

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Date: 20 October 2008

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